Electronic Circuits

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Power Amplifiers

Definitions

In small-signal amplifiers the main factors are:

- Amplification
- Linearity
- Gain

 $Since \ large-signal, or \ power, \ amplifiers \ handle \ relatively \ large$ voltage signals and current levels, the main factors are:

- · Maximum power capability
- Impedance matching to the output device

Amplifier Types

The amplifier conducts through the full 360° of the input. The Q-point is set near the middle of the load line.

The amplifier conducts through 180° of the input. The Q-point is set at the cutoff point.

Class AB

This is a compromise between the class A and B amplifiers. The amplifier conducts somewhere between 180° and 360°. The Q-point is located between the mid-point and cutoff.

Amplifier Types

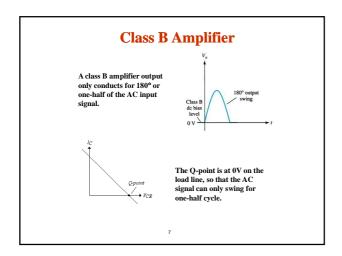
The amplifier conducts less than 180 of the input. The Q-point is located below the cutoff level.

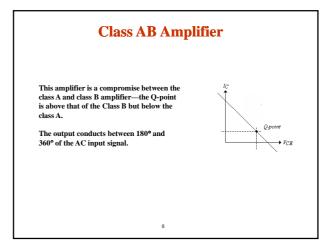
Class D

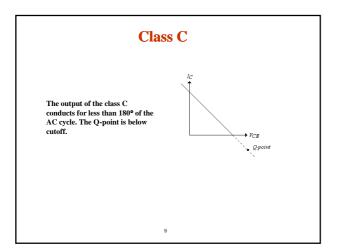
This is an amplifier that is biased especially for digital signals.

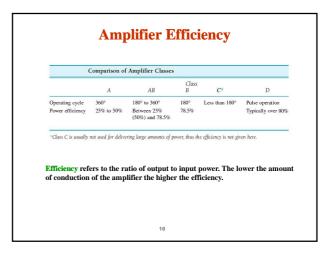


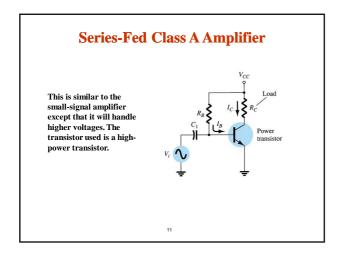
Class A Amplifier The output of a class A amplifier conducts for the full 360° of the cvcle. The Q-point is set at the middle of the load line so that the AC signal can swing a full cycle. Remember that the DC load line indicates the maximum and minin limits set by the DC power supply.

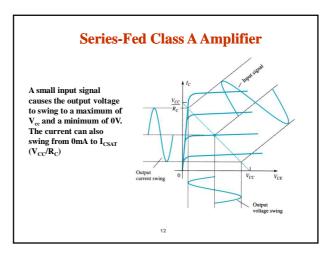




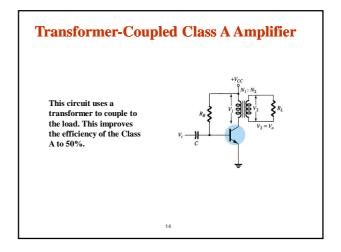


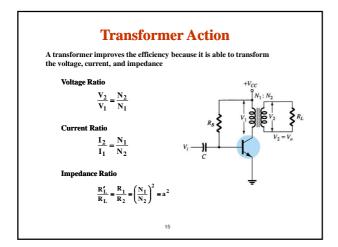


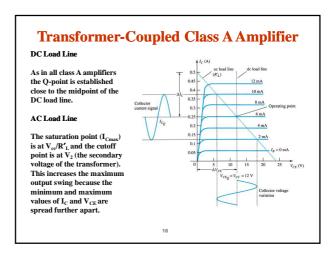


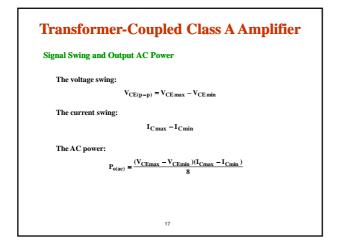


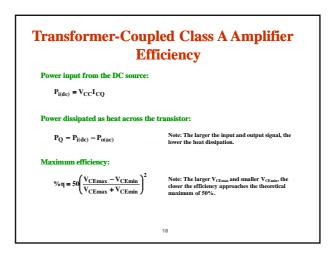
Series-Fed Class A Amplifier put Power The power into the amplifier is from the DC supply. With no input signal, the DC current drawn is the collector bias current, I_{CQ} . $P_{I(dc)} = V_{CC}I_{CQ}$ Output Power $P_{o(ac)} = \frac{V^2_{C(rms)}}{R_C} \qquad \text{or} \qquad P_{o(ac)} = \frac{V^2_{CE(p-p)}}{8R_C}$ Efficiency $\%\eta = \frac{P_{o(ac)}}{P_{I(ac)}} \times 100$











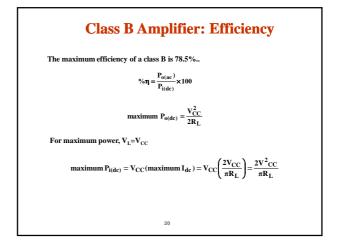
Class B Amplifier

In class B, the transistor is biased just off. The AC signal turns the transistor on.

The transistor only conducts when it is turned on by one-half of the AC cycle

In order to get a full AC cycle out of a class B amplifier, you need two transistors:

• An npn transistor that provides the negative half of the AC cycle
• A pnp transistor that provides the positive half.



Transformer-Coupled Push-Pull Class B Amplifier

The center-tapped transformer on the input produces opposite polarity signals to the two transistor inputs.

The center-tapped transformer on the output combines the two halves of the AC waveform together.

Class B Amplifier Push-Pull Operation

During the positive half-cycle of the AC input, transistor Q₁ (npn) is conducting and Q₂ (pnp) is off.

During the negative half-cycle of the AC input, transistor Q₂ (pnp) is conducting and Q₁ (npn) is off.

Each transistor produces one-half of an AC cycle. The transformer combines the two outputs to form a full AC cycle.

Crossover Distortion

If the transistors Q_1 and Q_2 do not turn on and off at exactly the same time, then there is a gap in the output voltage.

Quasi-Complementary Push-Pull Amplifier

A Darlington pair and a feedback pair combination perform the push-pull operation. This increases the output power capability.

Amplifier Distortion

If the output of an amplifier is not a complete AC sine wave, then it is distorting the output. The amplifier is non-linear.

This distortion can be analyzed using Fourier analysis. In Fourier analysis, any distorted periodic waveform can be broken down into frequency components. These components are harmonics of the fundamental frequency.

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Harmonics

Harmonics are integer multiples of a fundamental frequency.

If the fundamental frequency is 5kHz:

 1st harmonic
 1 x 5kHz

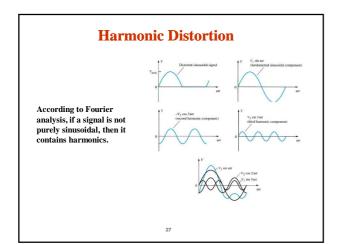
 2nd harmonic
 2 x 5kHz

 3rd harmonic
 3 x 5kHz

 4th harmonic
 4 x 5kHz

Note that the 1^{st} and 3^{rd} harmonics are called odd harmonics and the 2^{nd} and 4^{th} are called even harmonics.

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Harmonic Distortion Calculations

Harmonic distortion (D) can be calculated:

% nth harmonic distortion = %D_n = $\left| \frac{A_n}{A_1} \right| \times 100$

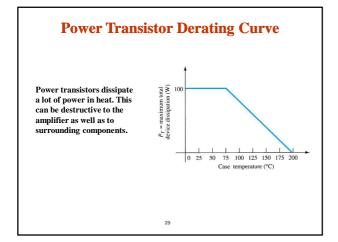
where

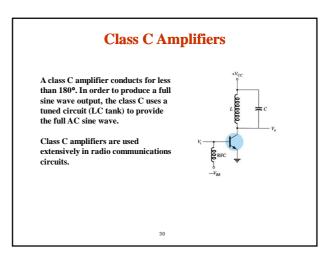
 \mathbf{A}_{n} is the amplitude of the fundamental frequency \mathbf{A}_{n} is the amplitude of the highest harmonic

The total harmonic distortion (THD) is determined by:

% THD =
$$\sqrt{D_2^2 + D_3^2 + D_3^2 + \cdots} \times 100$$

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Class D Amplifier A class D amplifier amplifies pulses, and requires a pulsed input. There are many circuits that can convert a sinusoidal waveform to a pulse, as well as circuits that convert a pulse to a sine wave. This circuit has applications in digital circuitry.