

Introduction to Cryptology

Lecture-15
Identification Protocols
Physical Security

22.05.2023, v51

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Cryptographic Identification Mechanisms/Protocols

1. Secret Key Mechanisms
2. Public Key Mechanisms

Required for every solid security system!

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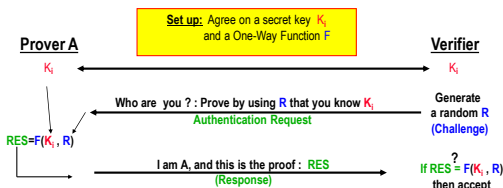
1. Secret-Key Identification Mechanisms

Require a secret key agreement!

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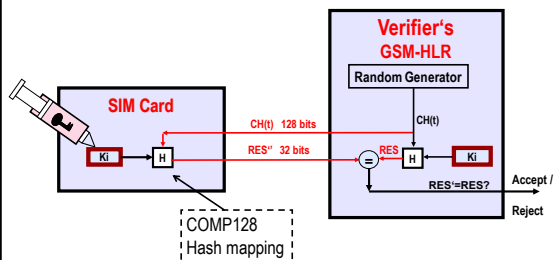
Challenge-Response Identification Mechanisms Explicit Secret Key Signature Authenticity without Secrecy



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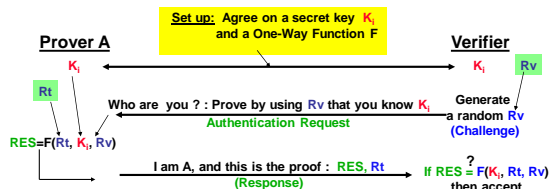
GSM Challenge-Response Identification (SIM-Card identification)



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Improved Symmetric Challenge-Response Identification Mechanism (Standard usage in modern network protocols)



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Template of Schnorr's Identification/signature Scheme

Prover A
 Secret key $x_A < q$
 $y_A = \alpha^{x_A} \text{ mod } p$

Open Directory (as DH public directory)
 $GF(p)$, Element α has order q such that q is prime which divides $p-1$
 y_A : public key of signer A

verifier

User A signs a message M:

- 1 Random $k \in GF(q)$ $r = \alpha^k \text{ mod } p$
A good and strong hash H function is required
- 2 Hash: $m = H(M | r)$ Similarity to ElGamal Signature!
- 3 Signature $\Rightarrow S = (k^{-1}x_A m) \text{ mod } q$
- 4 **A signed message M is: M, (S,m)**

- 5 $r' = \alpha^S y_A^m \text{ mod } p$
- 6 Compute $H(M | r')$
- 7 **Verification**
 if $m = H(M | r')$
 Then, M signed by A is authentic

Notice that $r' = \alpha^{-k^{-1}x_A m} \alpha^{(x_A)m} = \alpha^k$

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Physical Security

1. Why Physical Security
2. Physical Unclonability

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Why Physical Security?

Why **unclonable** physical units?

- Commercial-economic reasons (Cloning)
- Identity (Privacy)
- Know-How protection (IP-Cores)
- Medical
- Automotive units
- E-Money
- Smart-Home, -City, -Gouvernement, Consumer, **IoT** ..

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Popular Attack Scenarios

Physical Replacement/Substitution Attack

Examples:

- False mobile base station attack (GSM Mobile system)
- False Internet host

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Popular Attack Scenarios

Tele-service attack: Channel Hijacking Attack

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Popular Attack Scenarios

Network unit replacement attack

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Popular Attack Scenarios

Replacement Attack- Hardware patch

- Manufacturer cheats!
- Attacker has successfully cloned the unit!

Requirements

1. Unit structure should be unclonable or clone-resistant!
2. Unit should be unchangeable "Tamper-Proof"!

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Two Basic Requirements for Solid Security

1. Identify and trust each other (**Mutual-Authentication**)
2. Establish a secured link (**secrecy**)

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Contemporary State of the Art

- Identify & Trust (**Authentication**) Physical Security
 - Physical uniqueness !! ?
 - Unclonable units !! ?
 - Electro-Mechanical identity (Mechatronic-Identity) !! ?
(Automobile, Production Machines, Robots)
- Secured link (**secrecy**)
Relatively mature technology

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State of the Art for basically clonable Physical Units

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Practical GSM Security Gaps

International Mobile Equipment Identity
IMEI (non-secured)

Subscriber Identity Module
SIM (secured)

Cloned for some standard function
(150 000 challenges. Berkeley Univ.)
No collapse. System still solid!

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Attack on GSM device Identity IMEI

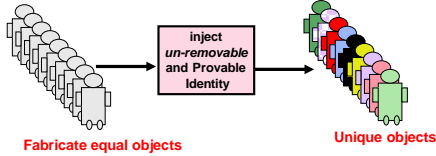
Mobile serial number IMEI (International Mobile Equipment Identity)

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Physically Unique Units Production

Common technique:

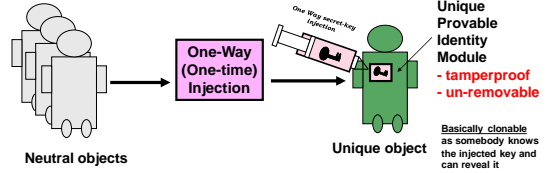


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Personalizing Physical Entities : One-Way Injection

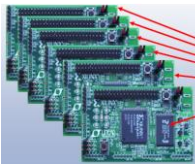
Mechanical simulation



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Requirements on Physical Uniqueness in Production



Personalization requirements:

- Uniqueness
- **Unclonable, Clone-Resistant**
- Remotely and securely Identifiable
- Low cost
- Resilient security if cloned! (brake-one brake-all impossible!)

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State of the Art in Unclonable Physical Units

DNA-like Physically Unclonable Functions (PUF's)

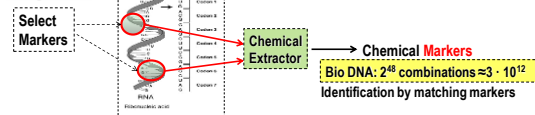
Bio-Inspired Provable Physical Identity
Make use of born uniqueness properties (DNA like)

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Best physical Identity: As the born **DNA-like** provable identification

Biological DNA



Physical Unclonable Functions. PUFs offer DNA like Identification Techniques

Ideal PUFs are: Born unpredictable and unclonable physical VLSI properties.

In other words: PUFs are analogue non-linear, hard to model or to copy, unpredictable huge mapping in a semiconductor VLSI device



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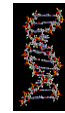
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State of the Art: Unclonable Devices by: Analog Physical Unclonable Functions (PUFs)

Since 2000 many proposals

- optical PUF
- coating PUF
- Silicon PUF
- optical fiber PUF
- RF COA
- LC-PUF
- S-RAM PUF
- Arbiter PUF
- fluorescent PUF

So far all have "Reproducibility" problems! Analog functions!!!



Unclonable

DNA-Chain

Delay PUF

Butterfly PUF

diode breakdown PUF

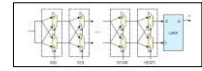
reconfigurable PUF

acoustic PUF

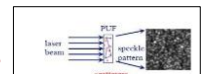
controlled PUF

photonic PUF

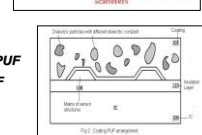
Born properties: Similar to the biological DNA



Delay based



Optical



Coating PUF (Capacity)

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PUFs inconsistency and aging difficulties

* Source: Roel Maes, ESAT/COSIC, K.U.Leuven, CRYPT Workshop

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Fuzzy Extractor for (PUFs)

□ **Fuzzy Extractor:** is used to correct errors in reproducing the PUF response by deploying error correcting codes

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Bio-Inspired Identification Protocol

DNA-Like Marker-Based Identification

DNA-Like Marker-Based Identification

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Best physical Identity: the born DNA-like provable identification

Biological DNA

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Selected Proposed Physical Unclonable Functions PUFs

Few promising PUF's

- Silicon PUF
- Intrinsic PUF
- Optical PUF
- Coating PUF

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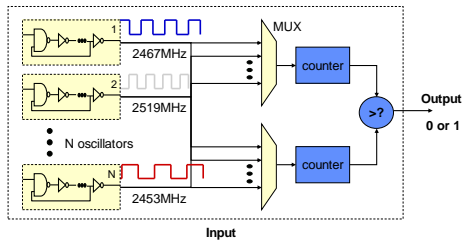
Silicon PUF MUX Chain or Arbiter PUFs

Principle: Compare delay time in a chain. Each physical chips exhibits own different delay time behaviour which identifies it

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Silicon PUF Ring Oscillator Design Procedure



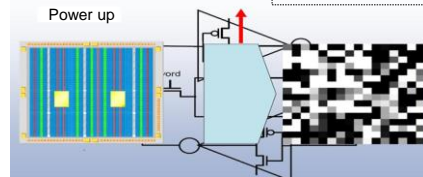
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Intrinsic PUF(I-PUF)

S-RAM PUF

S-RAM initial state after power on is mostly the same. (re-production !!)
However different from chip to chip



Source [8]

5.

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Intrinsic PUF(I-PUF)

SRAM PUF : Intra-Chip Variation

Principle: Read some memory area to identify physical units
S-RAM initial state after power on is mostly the same. (re-production !!)
However different from chip to chip



Source [8]

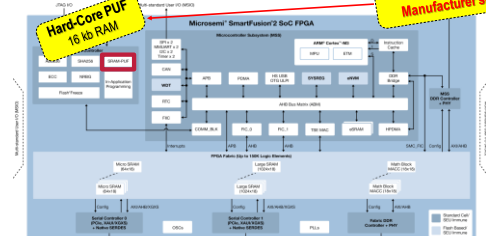
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Real-Field SRAM PUF in the Market

- Microsemi SmartFusion®2 Techn. SRAM PUF

50-100 K Gate
Manufacturer secrets!



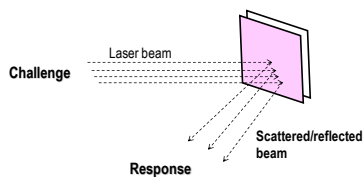
- Xilinx FPGA: SRAM PUF many IP-Cores ... SRAM, Delay-based, ...

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Optical PUF

Principle: Difference in reflected and refracted ray of light on a surface



Source [6]

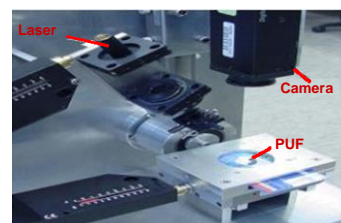
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Optical PUF

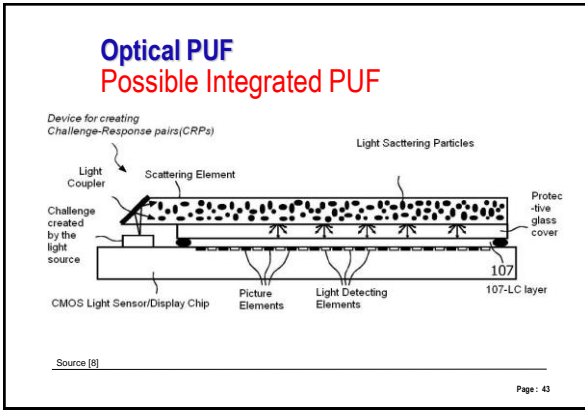
Practical set-up relatively complex



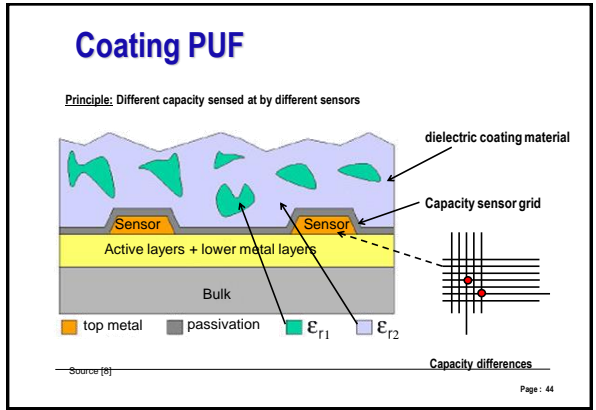
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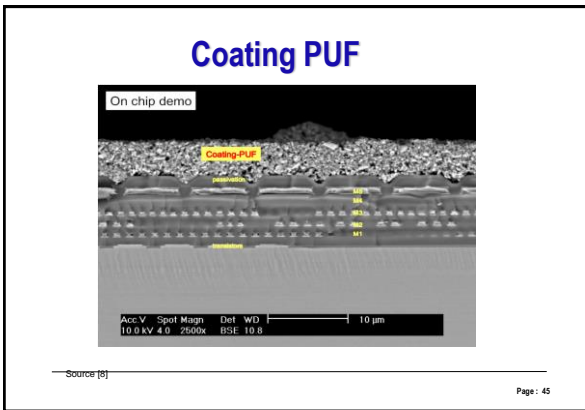
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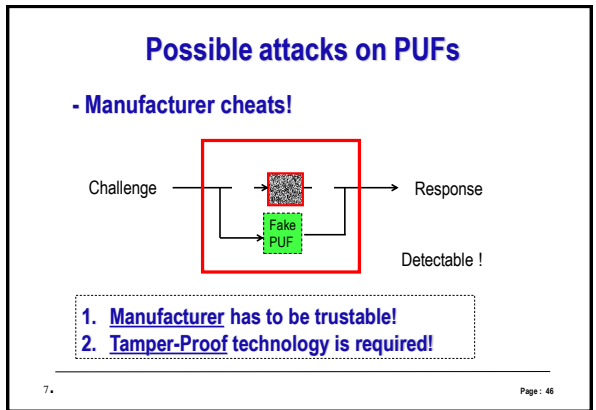
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All Analog PUFs are however,

1. Still complex and **costly** to produce
2. Sensitive to **temperature and supply voltage** etc.
3. Have long-term **reproducibility** "Aging" issues !
(non-Consistent in the long term!)

Still relatively limited for real field use!

Source [8]

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